



	IN '	THE		
UNITED STATES	PATENT	AND	TRADEMARK	OFFICE

Inventor(s):
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Heon-Deok Park, et al.

Confirmation No.

4103

Serial No.:

10/003,180

Examiner:

DeCady, Albert

Filing Date:

October 30, 2001

**Group Art Unit:** 

2133

Title:

SEMICONDUCTOR TEST SYSTEM AND METHOD FOR EFFECTIVELY TESTING A

SEMICONDUCTOR DEVICE HAVING MANY PINS

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INFORMATION DISCLOSURE STATEMENT

Technology Center 2100

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	This I	nformation Disclosure Statement is submitted:
	$\boxtimes$	under 37 CFR 1.97 (b), or
		(Within three months of filing national application; or date of entry of International application;
	o	r before mailing date of first office action on the merits; whichever occurs last)
		under 37 CFR 1.97 (c) together with either a:
		Statement under 37 CFR 1.97 (e), or
		a \$180 fee under 37 CFR 1.17 (p), or
		(After mailing of first Office Action, but prior to Notice of Allowance or Final Office Action)
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Applicant(s) submit herewith Form PTO 1449- Information Disclosure Citation together with copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

It is requested that the information disclosed herein be made of record in this application.

Any deficiency or overpayment should be charged or credited to deposit account number 13-1703. A duplicate copy of this sheet is enclosed.



PATENT TRADEMARK OFFICE

Respectfully submitted,

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